

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		09989777
	Filing Date		2001-11-19
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Mary C. Jacob	
	Attorney Docket Number	CD01208M	

U.S. PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code:	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	4608502	B1	1986-08-26	Dijkmans et al.	Entire Document
	2	6373954	B1	2002-04-16	Malcolm et al.	Entire Document
	3	5900780	B1	1999-05-04	Hirose et al.	Entire Document
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	5	5973368	B1	1999-10-26	Pearce et al.	Entire Document
	6	5248843	B1	1993-09-28	Billings	Entire Document
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21	4999519	B1	1991-03-12	Kitsukawa et al.	Entire Document
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FOREIGN PATENT DOCUMENTS

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	1	USPTO Notice of Allowance for Application Number 10/011,214 (CD00191) dated 04/11/2005; 4 pages	<input type="checkbox"/>
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	6	Varma et al., "A Structured Test Re-Use Methodology for Core-Based System Chips," IEEE, 1998; 9 pages	<input type="checkbox"/>
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8	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE; 6 pages	<input type="checkbox"/>
9	Ghosh et al., "A Low Overhead Design for Testability and Test Generation Technique for Core-based Systems," IEEE, 1997; 10 pages	<input type="checkbox"/>
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12	Papachristou et al., "Microprocessor Based Testing for Core-Based System on a Chip," IEEE, 1999; 6 pages	<input type="checkbox"/>
13	Maroufi et al., "Solving the I/O Bandwidth Problem in System on a Chip Testing," IEEE, 2000; 6 pages	<input type="checkbox"/>
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15	York et al., "On-chip Support Needed for SOC Debug," Electronic Engineering Times, 1999; 2 pages	<input type="checkbox"/>
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17	Hwang et al., "Integrated circuit for automatically varying resistance in computer system, has pair of transistors connected in parallel with respective resistors such that resistors are bypassed when corresponding transistors are enabled," Derwent Information LTD; 2002; 2 pages	<input type="checkbox"/>
18	Morrison, "IBM Eyes Merchant Packaging Services," July 13, 1998; Electronic News < http://www.findarticles.com >; 4 pages	<input type="checkbox"/>

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19	Charles, Jr. et al., "Wirebonding: Reinventing the Process for MCMs," April 1998; IEEE 7th International Conference on Multichip Modules and High Density Packaging; 3 pages	<input type="checkbox"/>
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39	Application Number 09/721,316 (CD98087C1): "Programmable Oscillator Scheme;" Mar et al.; 26 pages	<input type="checkbox"/>
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41	Application Number 09/998,859 (CD01209M): "A System and a Method for Checking Lock Step Consistency between in Circuit Emulation and a Microcontroller while Debugging Process is in Progress," Nemecek; 33 pages	<input type="checkbox"/>
42	Application Number 09/998,834 (CD01221M): "A System and a Method for Communication between and Ice and a Production Microcontroller while in a Halt State," Nemecek; 33 pages	<input type="checkbox"/>
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44	Application Number 09/989,574 (CD01164M): "Method and System for using a Graphics user Interface for Programming an Electronic Device," Bartz et al.; 43 pages	<input type="checkbox"/>
45	Application Number 09/989,816 (CD01165M): "Datasheet Browsing and Creation with Data-Driven Datasheet Tabs within a Microcontroller Design Tool," Bartz et al.; 55 pages	<input type="checkbox"/>
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